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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/044,707	01/11/2002	Dale E. Gulick	2000.052200/RSBTT4036	1281
23720	7590	02/08/2006		
WILLIAMS, MORGAN & AMERSON 10333 RICHMOND, SUITE 1100 HOUSTON, TX 77042			EXAMINER TO, JENNIFER N	
			ART UNIT	PAPER NUMBER
			2195	
DATE MAILED: 02/08/2006				

Please find below and/or attached an Office communication concerning this application or proceeding.

<b>Office Action Summary</b>	<b>Application No.</b>	<b>Applicant(s)</b>	
	10/044,707	GULICK, DALE E.	
	<b>Examiner</b>	<b>Art Unit</b>	
	Jennifer N. To	2195	

**-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --**  
**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

#### Status

- 1) ☒ Responsive to communication(s) filed on 17 November 2005.
- 2a) ☐ This action is **FINAL**.                      2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

#### Disposition of Claims

- 4) ☒ Claim(s) 1-28 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-28 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

#### Application Papers

- 9) ☒ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

#### Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All    b) ☐ Some \*    c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
  2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

#### Attachment(s)

- |   |   |
|---|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)             | 4) <input type="checkbox"/> Interview Summary (PTO-413)                     |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)    | Paper No(s)/Mail Date. _____  |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| Paper No(s)/Mail Date _____   | 6) <input type="checkbox"/> Other: _____                                    |

**DETAILED ACTION**

1. Claims 1-28 are pending for examination.
2. The disclosure is objected to because it contains an embedded hyperlink and/or other form of browser-executable code (specification, pages 13-21). Applicant is required to delete the embedded hyperlink and/or other form of browser-executable code. See MPEP § 608.01.

***Claim Rejections - 35 USC § 112***

3. The following is a quotation of the first paragraph of 35 U.S.C. 112:

The specification shall contain a written description of the invention, and of the manner and process of making and using it, in such full, clear, concise, and exact terms as to enable any person skilled in the art to which it pertains, or with which it is most nearly connected, to make and use the same and shall set forth the best mode contemplated by the inventor of carrying out his invention.
4. Claims 8-14, and 22-28 are rejected under 35 U.S.C. 112, first paragraph, as failing to comply with the enablement requirement. The claim(s) contains subject matter in which was not described in the specification in such a way as to enable one skilled in the art to which it pertains, or with which it is most nearly connected, to make and/or use the invention.
5. As per claims 8, and 22, these claims recited the limitation of “the task picker” in which described in the specification for selecting tasks from the queue based on priority scheme for execution (specification page 27, lines 2-3; applicant ‘s argument filed on 11/17/2005, page 10, line 5-7). However, there is nowhere in the claims supported

such function performed by the task picker (i.e. based on the claims, the controller is the one that selected tasks for executing not the task picker).

***Claim Rejections - 35 USC § 102***

6. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(a) the invention was known or used by others in this country, or patented or described in a printed publication in this or a foreign country, before the invention thereof by the applicant for a patent.

7. Claims 1-28 are rejected under 35 U.S.C. 102(a) as being anticipated by Shi et al. (hereafter Shi) (U.S. Patent No. 6757897).

8. As per claim 1, Shi teaches the invention as claim including a computer implemented method comprising:

storing one or more tasks in a queue (fig. 2; col. 13, lines 52-67; col. 14, lines 1-8), wherein each task has an associated exit routine (figs. 4, 6; col. 14, lines 34-67);

determining at least one task to process based on a priority scheme (fig. 3; col. 5, lines 47-54; col. 14, lines 15-30);

processing the at least one task (fig. 3; col. 5, lines 55-56; col. 14, lines 30-33);

and

calling the exit routine based on determining that the task is not completed processing within a pre-selected period of time (figs. 4, 6; col. 14, lines 34-53).

9. As per claim 2, Shi teaches that wherein storing the one or more task in the queue comprises storing at least one task in the queue at every pre-selected time interval (figs. 2-3; col. 12, lines 43-47; col. 13, lines 53-67; col. 14, lines 1-13).

10. As per claim 3, Shi further teaches generating an interrupt and wherein storing the one or more tasks in the queue comprises storing the one or more tasks in the queue in response to detecting the interrupt (figs. 2-3; col. 13, lines 52-67; col. 14, lines 1-67; col. 15, lines 1-67; col. 16, lines 1-7).

11. As per claim 4, Shi teaches that wherein determining at least one task to process based on the priority scheme comprises determining the at least one task based on a first-in, first-out priority scheme (fig. 2; col. 12, lines 43-47).

12. As per claim 5, Shi teaches that wherein the exit routine comprises calling the exit routine if the task does not complete executing within a pre-selected amount of time (fig. 6; col. 22, lines 13-69).

13. As per claim 6, Shi teaches that wherein calling the exit routine comprises terminating the task currently processing and returning control to a task picker in the queue (col. 14, lines 60-67; col. 15, lines 1-25; col. 22, lines 40-67; col. 23, lines 1-17).

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14. As per claim 7, Shi teaches that wherein processing the at least one task comprises executing the task and programming a timer to generate an interrupt after a pre-selected time, wherein the pre-selected time corresponds to the amount of time required for the task to complete executing (col. 23, lines 18-44).

15. As per claim 8, Shi teaches the invention as claim including an apparatus comprising:

a queue having a task picker stored therein (fig. 2; primary task 2.1); and

a controller communicatively coupled to the queue (fig. 2; yield scheduler), the controller to:

determine if at least one task other than the task picker is stored in the queue (col. 15 lines 26-47);

execute the at least one task other than the task picker based on determining that the at least one task other than the task picker is stored in the queue (col. 15, lines 32-57); and

execute the task picker in response to executing the at least one task other than the task picker and continue executing the task picker until a pre-selected event occurs (col. 15, lines 58-67; col. 16, lines 1-7).

16. As per claim 9, Shi teaches that wherein the pre-selected event comprises detection of an interrupt (fig. 6; col. 14, lines 34-42).

17. As per claim 10, Shi teaches that wherein the pre-selected event comprises detection of another task being present in the queue (fig. 6; col. 14, lines 34-42).

18. As per claim 11, Shi teaches that wherein each task stored in the queue comprises an exit routine to terminate that task (fig. 6; col. 34-67).

19. As per claim 12, Shi teaches that wherein the controller comprises a controller of a south bridge in a computer system (col. 10, lines 40-43).

20. As per claim 13, Shi teaches that wherein the controller determines that more than one task is stored in the queue and wherein the controller selects a task to execute from the one or more tasks based on a priority scheme (figs. 2-3; col. 5, lines 47-54; col. 12, lines 43-47; col. 14, lines 15-30).

21. As per claim 14, Shi teaches that wherein the priority scheme is a first-in, first-out scheme (fig. 2; col. 12, lines 43-47).

22. As per claims 15-21, they are article claims that correspond to method claims 1-7. Therefore, they are rejected for the same reason as method claims 1-7 above.

23. As per claim 22, it is rejected for the same reason as claims 1, 8 above. In addition, Shi teaches a failure recovery timer to generate an interrupt at pre-selected

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time intervals, wherein each pre-selected time interval is greater than the time it takes for each of the tasks stored in the queue to execute (figs. 4, 6; col. 14, lines 38-53; col. Col. 23, lines 18-36).

24. As per claim 23, it is rejected for the same reason as claim 4 above.

25. As per claim 24, Shi teaches that wherein each task has an associated exit routine and wherein the controller terminates the task by calling the exit routine (figs. 4, 6; col. 14, lines 34-67; col. Col. 22, lines 13-54).

26. As per claim 25, Shi teaches that wherein the controller resets the failure recovery timer before executing the task (col. 23, lines 30-32).

27. As per claim 26, Shi teaches that wherein the controller determines if the task completes execution within the pre-selected time interval (figs. 4, 6; col. 14, lines 34-53) comprises:

- detecting a first failure recovery interrupt (fig. 6; col. 22, lines 31-35);

- causing an interrupt service routine to determine a task ID associated with a task executing at the time of the first failure recovery interrupt (fig. 6; col. 2, lines 35-39);

- logging the determined task ID (col. 22, lines 40-42).

- detecting a second failure recovery interrupt (col. 23, lines 18-27);



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determining a task ID associated with a task executing at the time of the second failure recovery interrupt (col. 23, lines 26-44); and

terminating the task executing at the time of the second failure recovery interrupt in response to determining that the two task IDs are the same (col. 23, lines 45-67; col. 24, lines 1-25).

28. As per claim 27, it is rejected for the same reason as claims 2-3, and 7 above.

29. As per claim 28, Shi teaches that wherein the controller resets the failure recovery timer before executing the task picker (col. 23, lines 18-44).

### ***Response to Arguments***

30. Applicant's arguments with respect to claims 1-28 have been considered but are moot in view of the new ground(s) of rejection.

### ***Conclusion***

31. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jennifer N. To whose telephone number is (571) 272-7212. The examiner can normally be reached on M-T 6AM- 3:30 PM, F 6AM- 2:30 PM.

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32. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Meng-Ai An can be reached on (571) 272-3756. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

33. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Jennifer N To  
Examiner  
Art Unit 2195

MENG-AI  
SUPERVISORY PATENT EXAMINER  
TECHNICAL